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86	US 6456608 B1	20020924	146	Adaptive vector correlator using weighting signals for spread-spectrum communications	370/335	370/342; 375/150	Lomp; Gary R.
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142	US 5818365 A	19981006	14	Serial to parallel conversion with phase locked loop	341/100	375/376	Hush; Glen et al.
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146	US 5801782 A	19980901	19	Analog video encoder with metered closed caption data on digital video input interface	348/473	348/468; 375/240.01	Patterson; James T.
147	US 5799041 A	19980825	86	Network for multimedia asynchronous transfer mode digital signal transmission and components thereof	375/259	370/265; 370/420; 370/490; 725/118; 725/119; 725/95	Szkopek; Lech A. et al.
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150	US 5764687 A	19980609	26	Mobile demodulator architecture for a spread spectrum multiple access communication system	375/147	370/335; 375/349	Easton; Kenneth D.
151	US 5757869 A	19980526	24	Apparatus and method for detecting frame synchronization pattern/word in bit-stuffed digital data frame	375/366	370/506; 370/522; 375/368	Sands; Jeffrey J. et al.

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152	US 5748891 A	19980505	75	Spread spectrum localizers	375/150	342/132; 342/145; 342/189	Fleming; Robert Alan et al.
153	US 5740411 A	19980414	9	Controllably switched phase locked loop circuits, systems, and methods	713/503	331/1A; 331/25; 375/376	Hearn; Alan S. et al.
154	US 5729680 A	19980317	165	Ad hoc initialization for wireless local area network	709/222	370/436; 375/132; 709/248	Belanger; Philip H. et al.
155	US 5710793 A	19980120	29	Error signal quantization method and hardware for mixed blind and decision directed equalization	375/232	375/235; 708/323	Greenberg; Craig Bradley
156	US 5701330 A	19971223	11	Serial communication method and apparatus	375/257	340/534; 340/825. 5; 340/825. 52; 375/353	Lippmann; Raymond et al.
157	US 5699117 A	19971216	55	Moving picture decoding circuit	348/390. 1	375/240. 26	Uramoto; Shinichi et al.
158	US 5694586 A	19971202	12	Controller using time-domain filter connected to a signal line to control a time at which signal line is sampled for receipt of information transfer signal	713/401	375/350	Eneboe; Michael K.
159	US 5694438 A	19971202	17	Method and apparatus for managing a data symbol received in a time diversity communication system	375/347	711/200; 711/220	Wang; Annie Yalay et al.
160	US 5687326 A	19971111	17	Methods and apparatus for implementing a high speed serial link and serial data bus	710/105	375/220; 710/106	Robinson; Jeffrey I.

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161	US 5684828 A	19971104	26	Wireless data module with two separate transmitter control outputs	340/10.2	340/10.4 1; 340/10.5 ; 340/3.9; 375/219	Bolan; Michael L. et al.
162	US 5675584 A	19971007	30	High speed serial link for fully duplexed data communication	370/284	327/407; 327/415; 370/466; 370/503; 370/535; 375/220	Jeong; Deog- Kyoona
163	US 5654985 A	19970805	17	Address tracking over repeater based networks	375/211	340/825. 52; 370/246; 370/392; 375/213; 713/162; 713/163; 714/713	Crayford; Ian S. et al.
164	US 5654979 A	19970805	34	Cell site demodulation architecture for a spread spectrum multiple access communication systems	375/142	370/342; 375/150	Levin; Jeffrey A. et al.

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165	US 5654698 A	19970805	168	Missile telemetry data interface circuit	340/870.01	340/825.64; 340/870.18; 340/870.19; 375/242; 455/98	Snelgrove; Andrew H. et al.
166	US 5651001 A	19970722	22	Method and apparatus for full duplex signaling	370/276	370/226; 370/279; 370/285; 375/288	Alvstad; Gary A. et al.
167	US 5644361 A	19970701	16	Subsampled frame storage technique for reduced memory size	375/240.16	375/240.21	Ran; Xiaonong et al.
168	US 5636294 A	19970603	29	Method and apparatus for truncation of images during data compression	382/239	358/426.05; 358/426.06; 375/240.03; 375/240.05; 375/240.2	Grosse; Debora I. Y. et al.
169	US 5630033 A	19970513	91	Adaptive threshold filter and method thereof	345/418	345/501; 348/699; 375/240.15	Purcell; Stephen C. et al.
170	US 5627860 A	19970506	24	Peak and valley detector for a selective call receiving device	375/317	340/7.43; 375/316; 375/340	McKinny; Carol A. et al.

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171	US 5608755 A	19970304	36	Method and apparatus for implementing carrierless amplitude/phase encoding in a network	375/219	370/201; 375/261	Rakib; Selim
172	US 5598429 A	19970128	15	Multi-level correlation system for synchronization detection in high noise and multi-path environments	375/149	370/515; 375/150; 375/367	Marshall; Kenneth E.
173	US 5587709 A	19961224	29	High speed serial link for fully duplexed data communication	341/100	375/371	Jeong; Deog-Kyoon
174	US 5581302 A	19961203	15	Subsampled frame storage technique for reduced memory size	375/240. 16	348/699; 375/240. 21	Ran; Xiaonong et al.
175	US 5557616 A	19960917	53	Frame synchronization in a performance monitoring and test system	370/509	375/357; 375/368	Cadieux; Kevin et al.
176	US 5537435 A	19960716	57	Transceiver apparatus employing wideband FFT channelizer with output sample timing adjustment and inverse FFT combiner for multichannel communication network	375/219	370/210; 370/280; 370/484; 375/232; 375/233; 375/260	Carney; Ronald et al.
177	US 5530483 A	19960625	106	Delay detector apparatus and method for plural image sequences	348/518	348/192; 375/240. 22; 382/209; 714/815	Cooper; James C. et al.
178	US 5524008 A	19960604	9	Generating real-time events in a TDMA digital wireless communications system	370/337	375/219	Levy; Jonathan

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179	US 5500853 A	19960319	63	Relative synchronization system for a telephone network	370/252	370/520; 375/357; 379/14.01; 714/712	Engdahl; Thomas L. et al.
180	US 5495470 A	19960227	64	Alarm correlation system for a telephone network	370/248	370/252; 370/516; 375/362; 379/14; 379/15.05; 379/33; 714/712	Tyburski; Edward et al.
181	US 5495469 A	19960227	21	Communications network, state machine therefor	370/212	340/3.1; 370/462; 375/238	Halter; Richard A. et al.
182	US 5488641 A	19960130	12	Digital phase-locked loop circuit	375/374	327/150; 370/518; 375/376	Ozkan; Oguz
183	US 5483539 A	19960109	19	Programmable PCM/TDM demultiplexer	370/509	370/514; 370/542; 375/368	Kaufmann; John
184	US 5471508 A	19951128	41	Carrier recovery system using acquisition and tracking modes and automatic carrier-to-noise estimation	375/344	375/261; 455/226.3	Koslov; Joshua L.

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185	US 5463662 A	19951031	20	Apparatus and method for reducing errors in data caused by noise through use of blanking	375/351	455/223	Sutterlin; Philip H. et al.
186	US 5448593 A	19950905	30	Frequency hopping time-diversity communications systems and transceivers for local area networks	375/267	370/389; 375/295; 375/345; 455/226.1; 714/704	Hill; Lawrence W.
187	US 5440591 A	19950808	10	Switching apparatus for digital signals	375/354	340/825.2; 370/517	Liron; John E. et al.
188	US 5421023 A	19950530	51	Motion vector calculation method using sequential minimum distortion calculations at different densities	348/701	375/240.16	Murakami; Tokumichi
189	US 5410572 A	19950425	21	Phase locked loop circuit	375/376	331/14	Yoshida; Koichi
190	US 5406590 A	19950411	11	Method of and apparatus for correcting edge placement errors in multiplying phase locked loop circuits	375/376	327/159; 327/175; 331/10; 714/700	Miller; Joseph P. et al.
191	US 5402448 A	19950328	14	Burst mode receiver control	375/340	370/514; 375/376	Marko; Paul D. et al.
192	US 5400369 A	19950321	9	Fram aligner with reduced circuit scale	375/368	370/510	Ikemura; Kuniichi
193	US 5400087 A	19950321	102	Motion vector detecting device for compensating for movements in a motion picture	348/699	375/240.15; 375/240.16	Uramoto; Shinichi et al.

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194	US 5394444 A	19950228	19	Lock detect circuit for detecting a lock condition in a phase locked loop and method therefor	375/374	327/156; 375/376	Silvey; John M. et al.
195	US 5325405 A	19940628	16	Burst mode receiver control	375/366	370/509	Marko; Paul D. et al.
196	US 5309428 A	19940503	56	Token ring local area network testing apparatus for phase jitter testing	370/245	370/452; 370/518; 375/226; 714/715; 714/717	Copley; Mark et al.
197	US 5260842 A	19931109	23	Data separator having an accurate delay circuit	360/51	360/48; 360/53; 375/359	Leake; William W. et al.
198	US 5258999 A	19931102	17	Circuit and method for receiving and transmitting control and status information	375/220	375/377	Wernimont; Thomas L. et al.
199	US 5258877 A	19931102	19	Data separator having a restart circuit	360/51	360/53; 375/376	Leake; William W. et al.
200	US 5179577 A	19930112	17	Dynamic threshold data receiver for local area networks	375/317	327/72; 455/219	Ilyadis; Nicholas
201	US 5115451 A	19920519	11	Local area network modem	375/221	370/249; 370/516; 375/355	Furlong; Darrell
202	US 5111451 A	19920505	16	Method and apparatus for synchronizing an optical transceiver over a full duplex data communication channel	370/294	370/516; 375/222; 375/376	Piasecki; Douglas S. et al.

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203	US 5050189 A	19910917	25	Multibit amplitude and phase modulation transceiver for LAN	375/223	370/516; 375/263; 375/290; 375/348; 375/371	Cox; William M. et al.
204	US 5036528 A	19910730	12	Self-calibrating clock synchronization system	375/374	327/231; 327/3; 331/18	Le; Duc N. et al.
205	US 4989221 A	19910129	12	Sample rate converter	375/222	370/290; 379/406.08	Qureshi; Shahid U. H. et al.
206	US 4951139 A	19900821	65	Computer-based video compression system	375/240.07	375/245	Hamilton; Eric R. et al.
207	US 4945548 A	19900731	34	Method and apparatus for detecting impending overflow and/or underrun of elasticity buffer	375/214	370/506; 375/372	Iannarone; John R. et al.
208	US 4897717 A	19900130	66	Computer-based video compression system	375/240.07	375/240; 375/240.22	Hamilton; Eric R. et al.
209	US 4873703 A	19891010	33	Synchronizing system	375/371	375/354; 375/356; 713/400; 713/501	Crandall; Douglas et al.

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210	US 4829258 A	19890509	17	Stabilized phase locked loop	327/156	327/157; 327/159; 327/227; 331/1A; 331/25; 375/376	Volk; Andrew M. et al.
211	US 4819081 A	19890404	17	Phase comparator for extending capture range	327/12	327/159; 327/237; 327/3; 331/1A; 375/373; 375/376	Volk; Andrew M. et al.
212	US 4766606 A	19880823	53	Signal repeater for multi subscriber communication over single pair telephone line	379/344	370/293; 375/214	Bardutz; Ronald W. et al.
213	US 4698672 A	19871006	19	Coding system for reducing redundancy	375/240. 12	358/1.9; 375/246	Chen; Wen- hsiung et al.
214	US 4680778 A	19870714	15	Fast acquisition circuit for synchronous digital receiver operating in wideband noise	375/344	375/376; 455/265	Krinoock; Jerome V.
215	US 4592072 A	19860527	15	Decoder for self-clocking serial data communications	375/282	329/311; 360/42; 375/328; 375/333; 375/360	Stewart; Robert E.

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216	US 4580134 A	19860401	36	Color video system using data compression and decompression	345/589	345/605; 375/240. 24	Campbell; Graham M. et al.
217	US 4571735 A	19860218	20	Method of multi-level encoding including synchronizing signals	375/293	341/57; 341/94; 360/40; 375/292; 714/810	Furse; Anthony G.
218	US 4528676 A	19850709	17	Echo cancellation circuit using stored, derived error map	375/232	327/552; 332/107; 333/18; 375/231; 375/293; 375/348	Mein; Gordon F. et al.
219	US 4501003 A	19850219	174	Dial pulse measurement circuitry	375/340	379/351; 714/715; 714/761	Miller; Hubert A.
220	US 4451916 A	19840529	99	Repeated, multi-channel fiber optic communication network having fault isolation system	714/4	370/226; 375/214; 380/2; 380/256; 380/268; 398/5; 455/16; 455/8; 455/9	Casper; Paul W. et al.
221	US 4450572 A	19840522	15	Interface for serial data communications link	375/333	360/44; 375/361	Stewart; Robert E. et al.

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222	US 4392159 A	19830705	79	Method and apparatus for digital video signal processing	386/12	348/500; 348/521; 360/32; 375/363; 386/40	Lemoine; Maurice G. et al.
223	US 4100533 A	19780711	28	Multipoint polling technique	340/825. 22	178/3; 340/825. 52; 340/825. 53; 370/449; 375/222	Napolitano; Orlando et al.
224	US 3944940 A	19760316	7	Versatile phase-locked loop for read data recovery	360/51	331/11; 331/27; 360/73.0 1; 375/327; 375/376	Desai; Ashok K.
225	US 3818347 A	19740618	18	RECEIVER FOR AMPLITUDE MODULATED QUADRATURE CARRIER SIGNALS	375/340	375/327; 375/376	Holsinger; Jerry L.